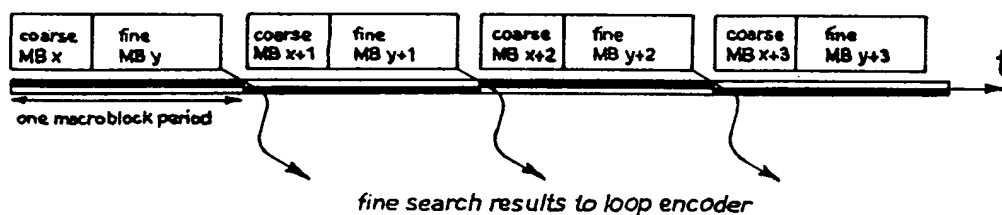


Fig. 1 is a block diagram of a video processing system. The system consists of a sequence of processing blocks connected in a pipeline. The blocks are labeled as follows:

- MV fifo** and **MVG**: The first block in the pipeline.
- pipe control word**: A block that receives a **pipe control word** (X mv, Y mv, pred type, etc.) and outputs to the next block.
- AG**: A block that outputs to the next block.
- XY to physical address**: A block that outputs to the next block.
- CMB & Prediction blocks addr.**: A block that outputs to the next block.
- PF and block cache management**: A block that outputs to the next block.
- prediction frame blocks lines**: A block that outputs to the next block.
- PA**: A block that outputs to the next block.
- Predictor lines**: A block that outputs to the next block.
- CFD**: A block that outputs to the next block.
- L1 distance calculation**: A block that outputs to the next block.
- prediction MAE**: A block that outputs to the next block.
- DEC**: A block that outputs to the next block.
- prediction error winning mv (XY)**: The final block in the pipeline, which outputs to the **loop encoder**.

The system also includes a **4-way B-cache** and an **STBUS I/F** (System Transfer Bus Interface). The **STBUS I/F** is connected to the **CFD** block and the **prediction error winning mv (XY)** block. It also has a connection to the **BUS/MEMORY** interface. The **STBUS I/F** is labeled with **R** (Read) and **R/W** (Read/Write) signals. The **BUS/MEMORY** interface is labeled with **W** (Write) and **R** (Read) signals. The **STBUS I/F** is also connected to a **loop encoder**.

Fig_2



Fig_3

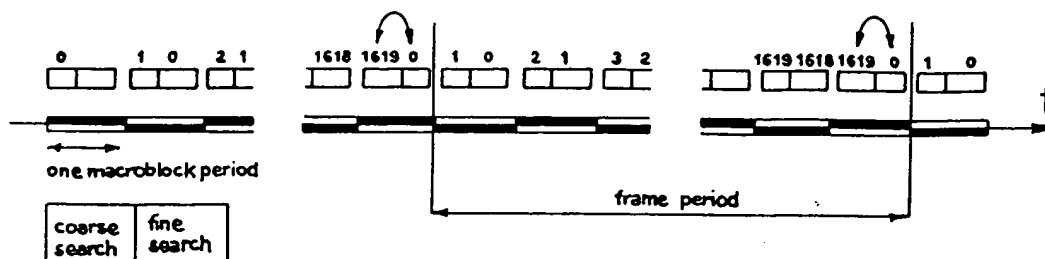
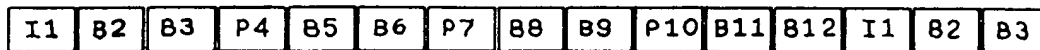
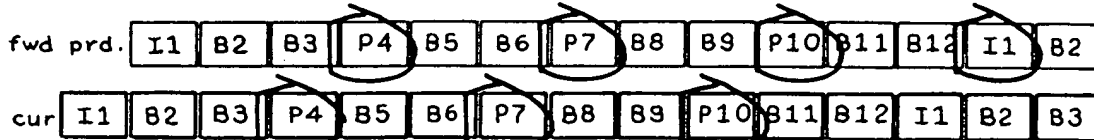


Fig. 4

INPUT FRAMES ORDER



COARSE SEARCH



FINE SEARCH

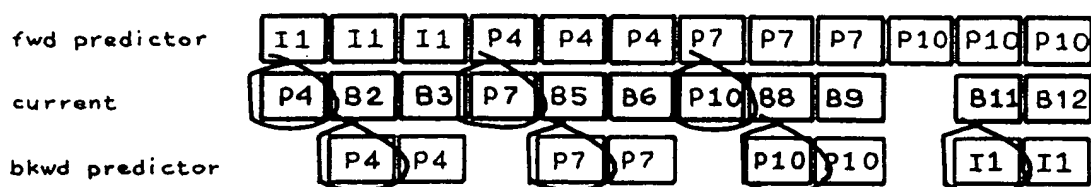
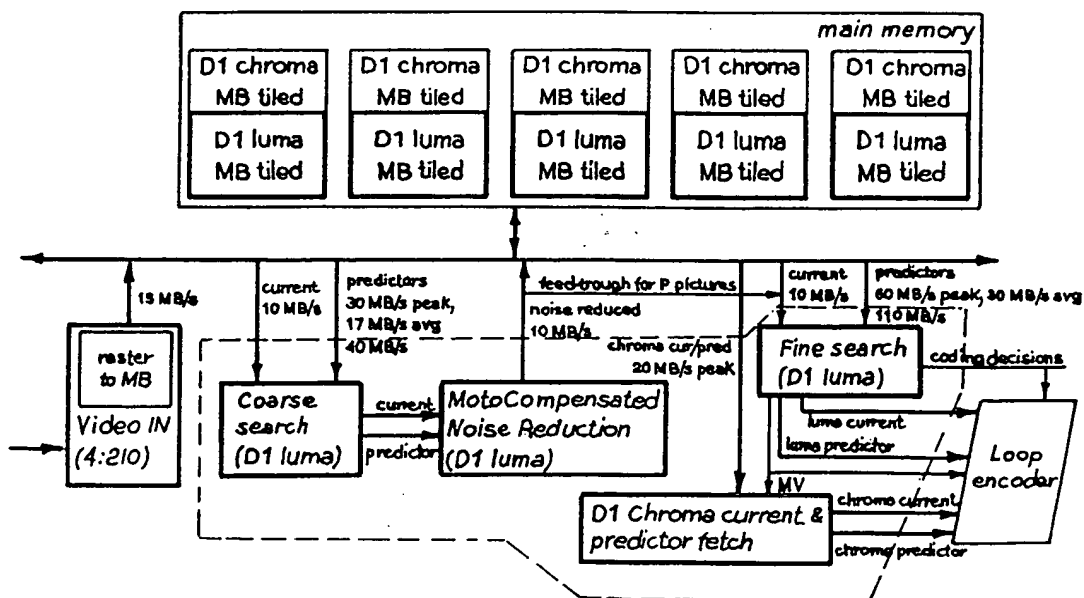
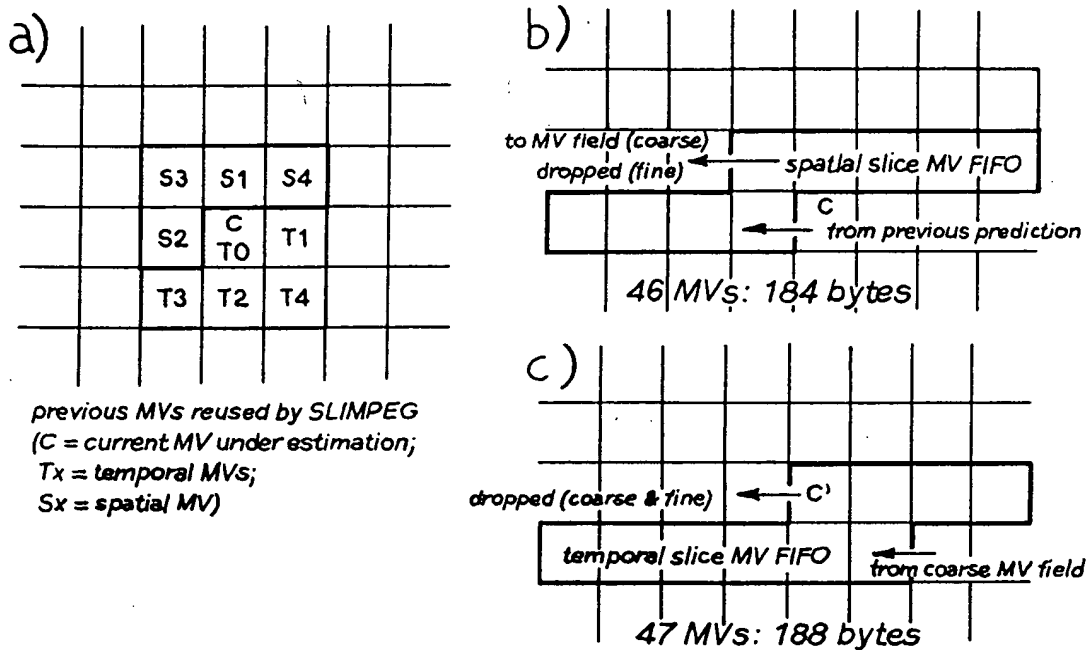


Fig. 5



Fig_6



Fig_9

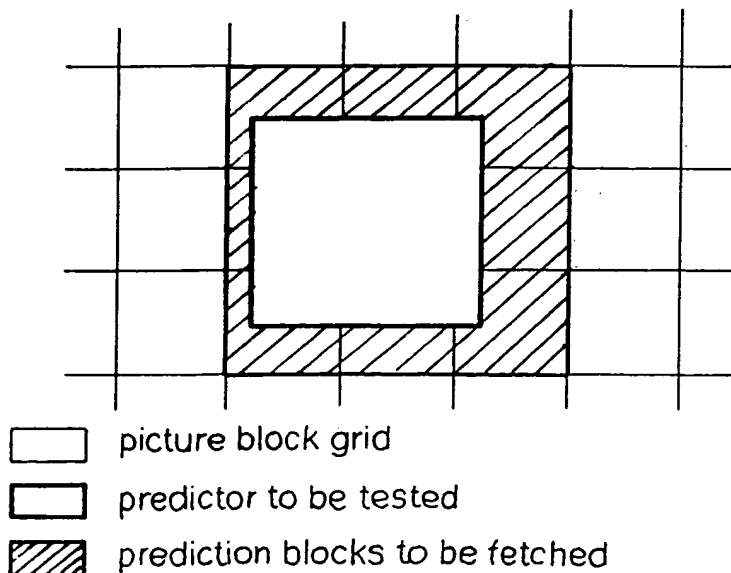


Fig. 7

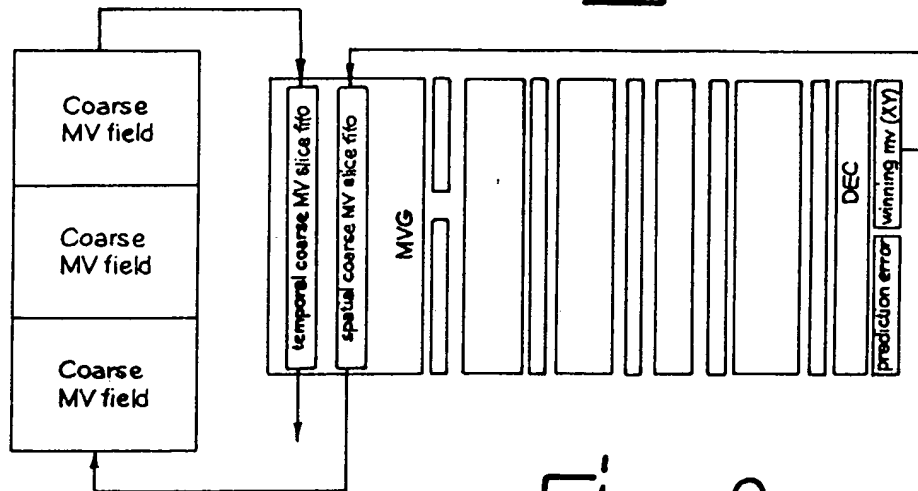


Fig. 8

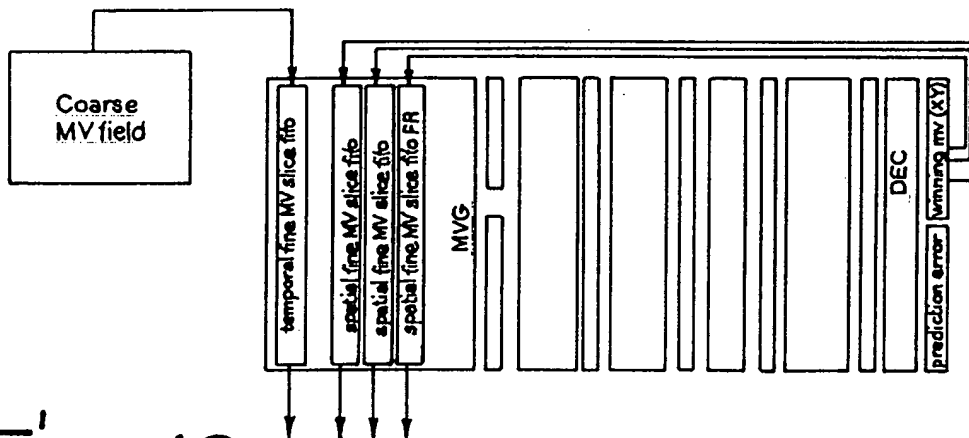
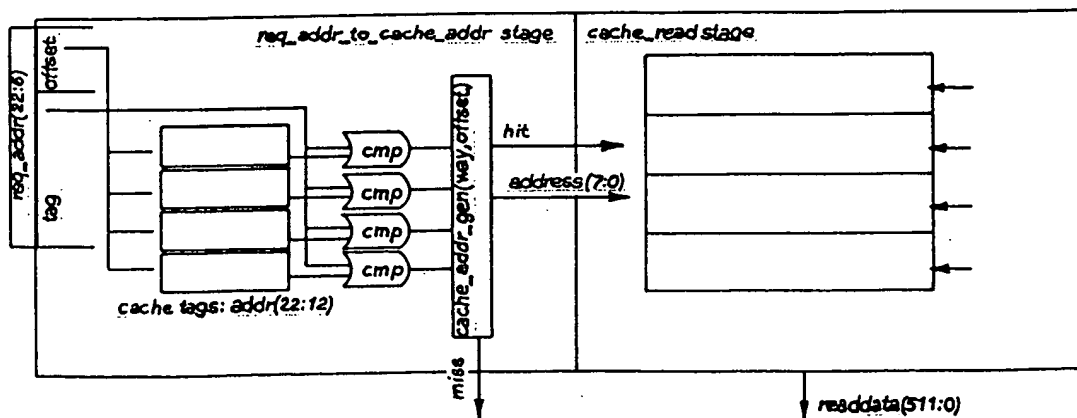
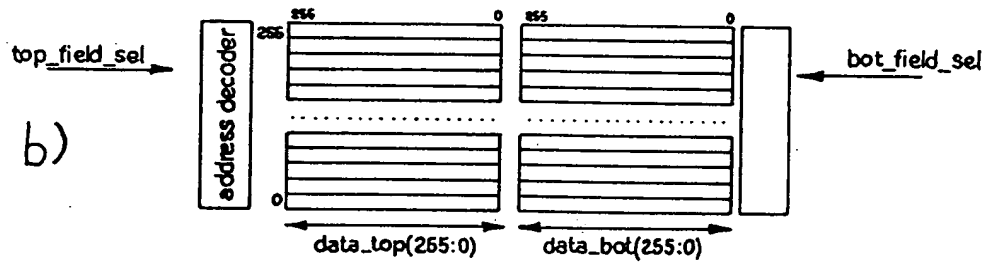
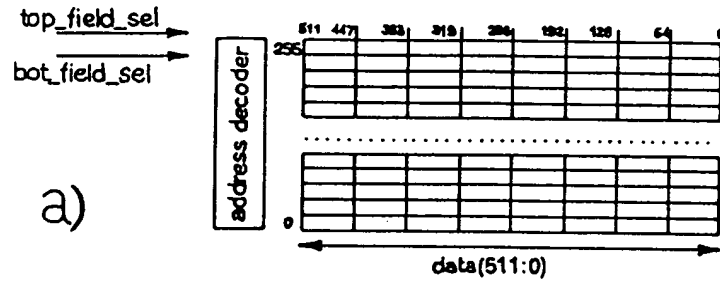


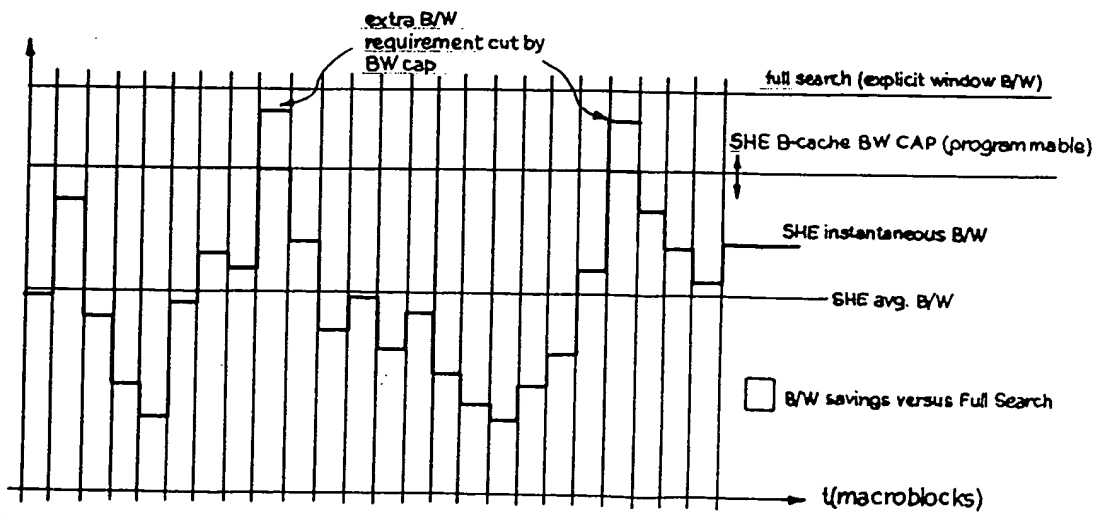
Fig. 10



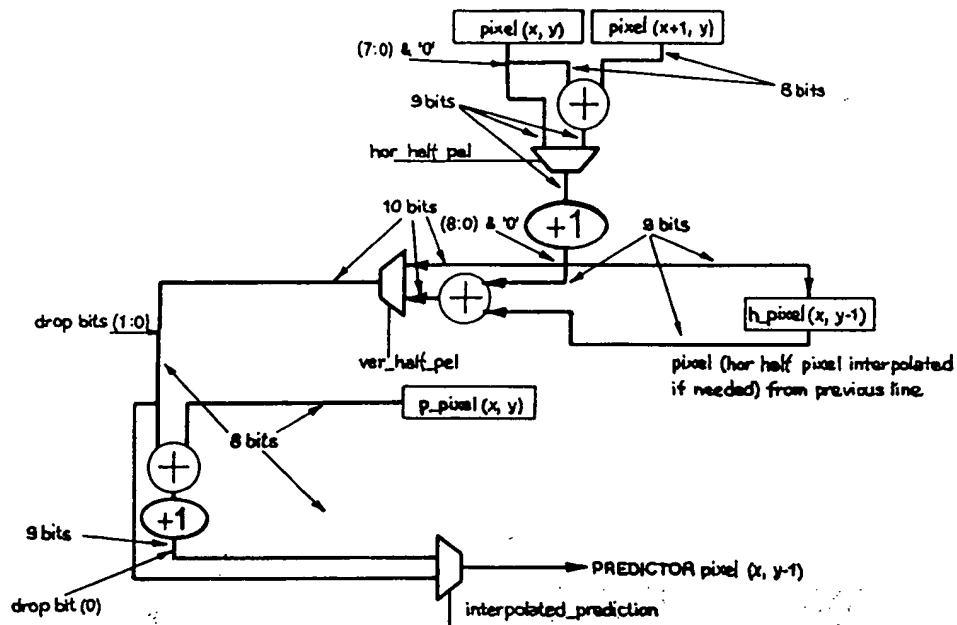
Fig_11



Fig_12



Fig_13



Fig_14

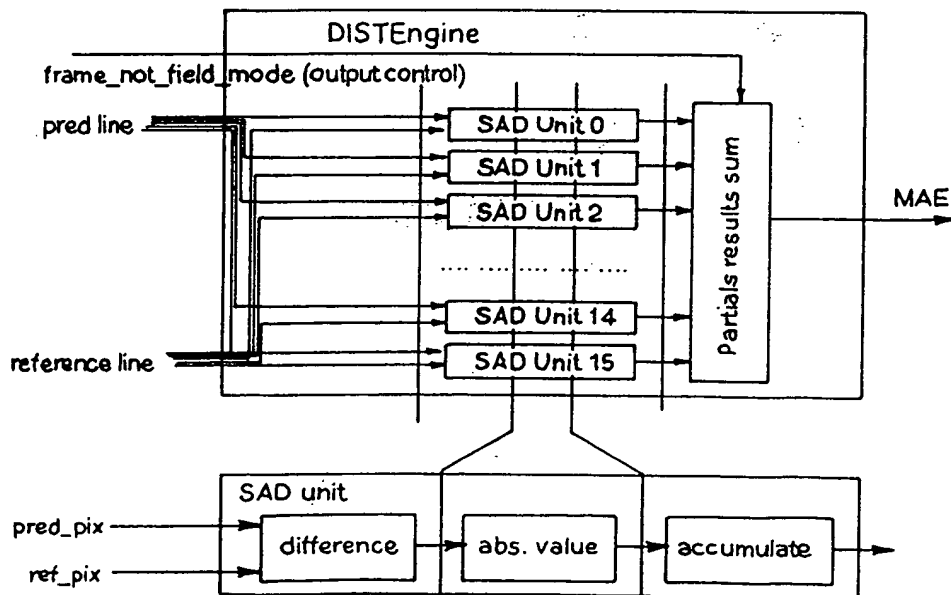


Fig. 15

